<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>COVER</td>
</tr>
<tr>
<td>2</td>
<td>Revision History</td>
</tr>
<tr>
<td>3</td>
<td>Block Diagram</td>
</tr>
<tr>
<td>4</td>
<td>COMe R2.0 Type6 RAW A/B</td>
</tr>
<tr>
<td>5</td>
<td>COMe R2.0 Type6 RAW C/D</td>
</tr>
<tr>
<td>6</td>
<td>Clock Buffer</td>
</tr>
<tr>
<td>7</td>
<td>PCI Express X16 Slot</td>
</tr>
<tr>
<td>8</td>
<td>DDI Slot</td>
</tr>
<tr>
<td>9</td>
<td>PCI Express X4 / X1</td>
</tr>
<tr>
<td>10</td>
<td>LAN / USB 2.0 Port 0-1</td>
</tr>
<tr>
<td>11</td>
<td>USB2.0 Port 2-3/ Port7</td>
</tr>
<tr>
<td>12</td>
<td>USB3.0 Port 0-3</td>
</tr>
<tr>
<td>13</td>
<td>CRT Connector</td>
</tr>
<tr>
<td>14</td>
<td>LVDS Connector</td>
</tr>
<tr>
<td>15</td>
<td>SIO W83627DHG</td>
</tr>
<tr>
<td>16</td>
<td>PS2 MINIDIN6</td>
</tr>
<tr>
<td>17</td>
<td>Parallel Port</td>
</tr>
<tr>
<td>18</td>
<td>COM 1-4 / SIR</td>
</tr>
<tr>
<td>19</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>20</td>
<td>Mini PCIe</td>
</tr>
<tr>
<td>21</td>
<td>Express Card Slot 1</td>
</tr>
<tr>
<td>22</td>
<td>Express Card Slot 2</td>
</tr>
<tr>
<td>23</td>
<td>HD Audio Codec ALC892</td>
</tr>
<tr>
<td>24</td>
<td>RTC / FAN</td>
</tr>
<tr>
<td>25</td>
<td>FWH: BIOS2</td>
</tr>
<tr>
<td>26</td>
<td>LPC to Port 80 / TPM</td>
</tr>
<tr>
<td>27</td>
<td>Miscellaneous</td>
</tr>
<tr>
<td>28</td>
<td>ATX power / +V5_DUAL</td>
</tr>
<tr>
<td>29</td>
<td>DC IN / +V3.3_DUAL</td>
</tr>
<tr>
<td>30</td>
<td>Power Map</td>
</tr>
</tbody>
</table>
LAN

I/O Plan

+V3.3 DUAL

USB 2.0 x 2 Ports

LAN / USB 2.0 Port 0-1

PS4

-5V_USB1--40mils

-5V_USB2--40mils

USB power consumption 500mA max

LAN1_USB01A RJ45+USBx2_W/XFMR <Characteristic>

LAN1_USB01B RJ45+USBx2_W/XFMR <Characteristic>

LAN1_USB01C RJ45+USBx2_W/XFMR <Characteristic>

5.5A

90_100MHZ

B31 <Characteristic>

2.0*1.2*1.3mm

B32 <Characteristic>

2.0*1.2*1.3mm

+V5_USB_0_1-->40mils

+V5_USB_0_1-->40mils

LAN1_USB1B

LAN1_USB1C

USB 2.0 x 2 Ports

Near the USB connector

Near the USB connector

LAN1_MDI0- 

LAN1_MDI0+ 

LAN1_MDI1- 

LAN1_MDI1+ 

LAN1_MDI2- 

LAN1_MDI2+ 

LAN1_MDI3- 

LAN1_MDI3+ 

LAN1_LINK# 

LAN1_LINK100# 

LAN1_LINK1000# 

LAN1_ACT# 

GBE0_CTREF 

LAN1_MDI0+ [4]

LAN1_MDI0- [4]

LAN1_MDI1+ [4]

LAN1_MDI1- [4]

LAN1_MDI2+ [4]

LAN1_MDI2- [4]

LAN1_MDI3+ [4]

LAN1_MDI3- [4]

LAN1_LINK100# [4]

LAN1_LINK100# [4]

LAN1_ACT# [4]

GBE0_CTREF [4]
USB power consumption 500mA max

USB2.0 Port 2-3/ Port7

USB power consumption 500mA max

USB 2.0 x 2 Ports
placement near the CRT connector

placement near the CRT connector

It change the default of VGA_HS that through the buffer to VGA connector

It change the default of VGA_VS that through the buffer to VGA connector
Pin to pin change to 1654009611
PCI Express Mini Card

placement close to Mini PCIe connector power pin
SMART FAN

RTC POWER
1-2 Clear CMOS
2-3 Normal Operation

SYS_FAN
To delete the double pull up resistor A101-2
Vout = 0.8(1 + (110/34.8)) = 3.328V

+VDC = 8.5V~19V

-CH MOSFET FDMS6681Z
RDS(on): 3.2m Ohm @ Vgs = -10V
RDS(on): 5m Ohm @ Vgs = -4.5V

DCIN_12V_SLT1(1-2) 1-2  AT Mode
2-3  ATX Mode

Add BAT54C to prevent the floating issue for DCIN function
Add Q38 to prevent the leaking voltage when DCIN voltage under 12V.

A101-3
Add the level shift circuit to solve can't control MOSFET(Q39) issue.