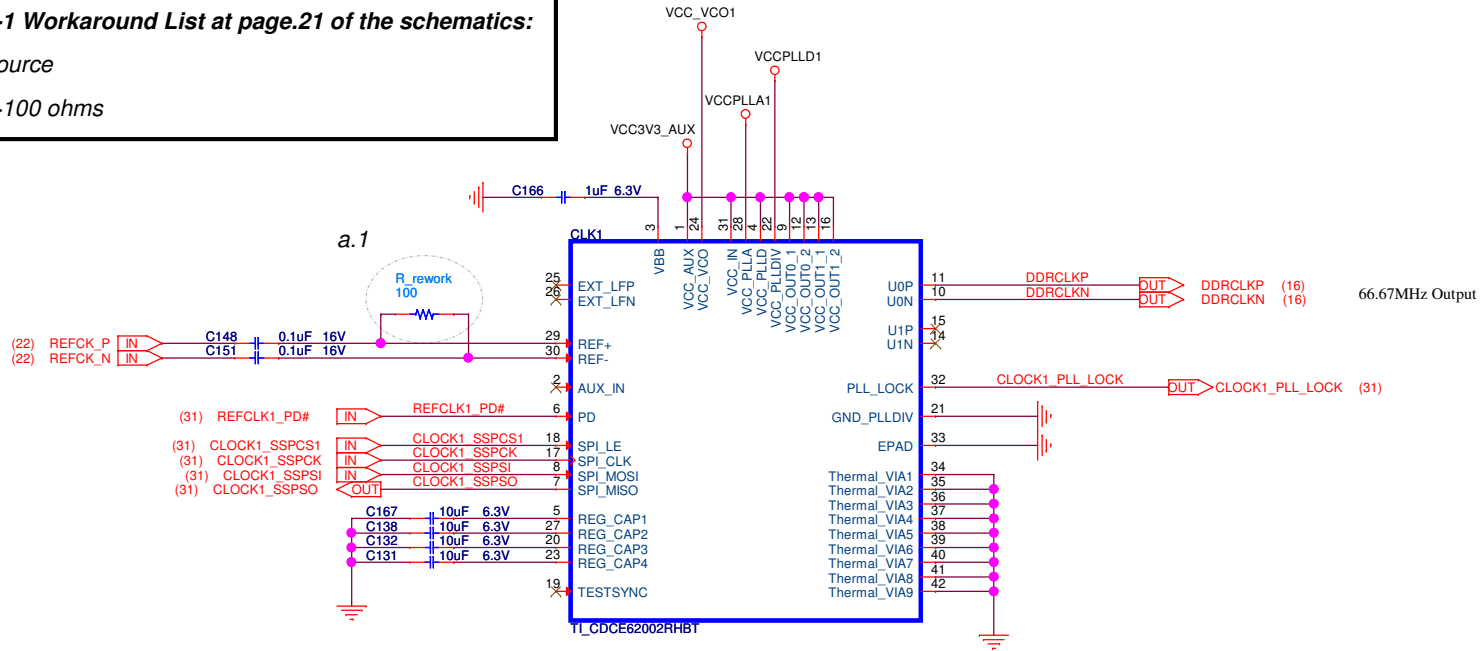


CLOCK GEN1 (DDR3)

TMDXEVM6670L EVM A102-1 Workaround List at page.21 of the schematics:

a. Improving the clock source

a.1 Additional R ->100 ohms



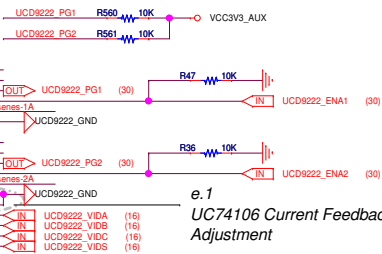
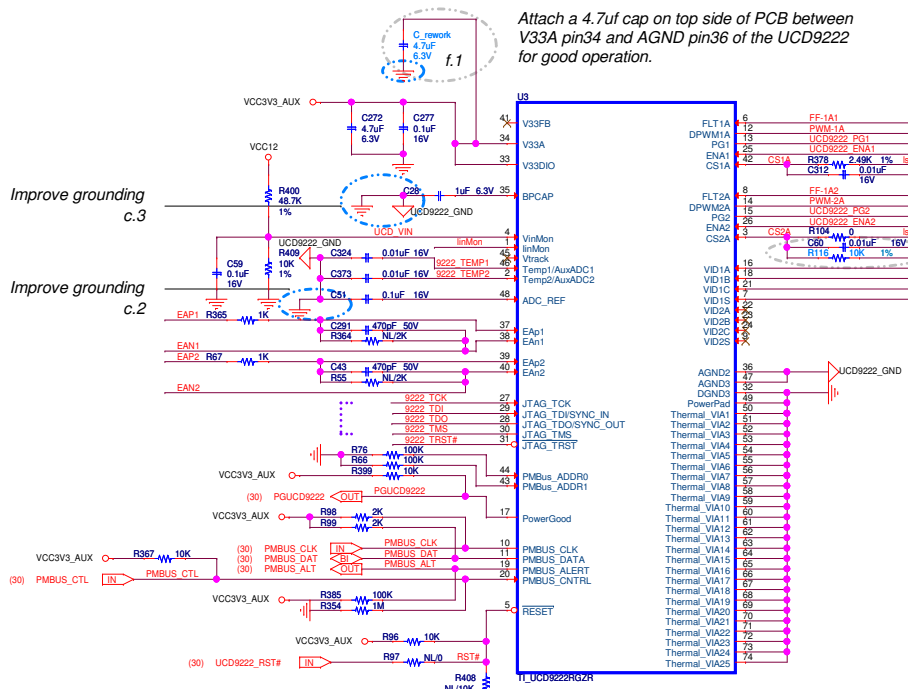
CVDD

Improve UCD9244 analog power

Attach a 4.7uf cap on top side of PCB between V33A pin34 and AGND pin36 of the UCD9222 for good operation.

Improve grounding
c.3

Improve grounding
c.2

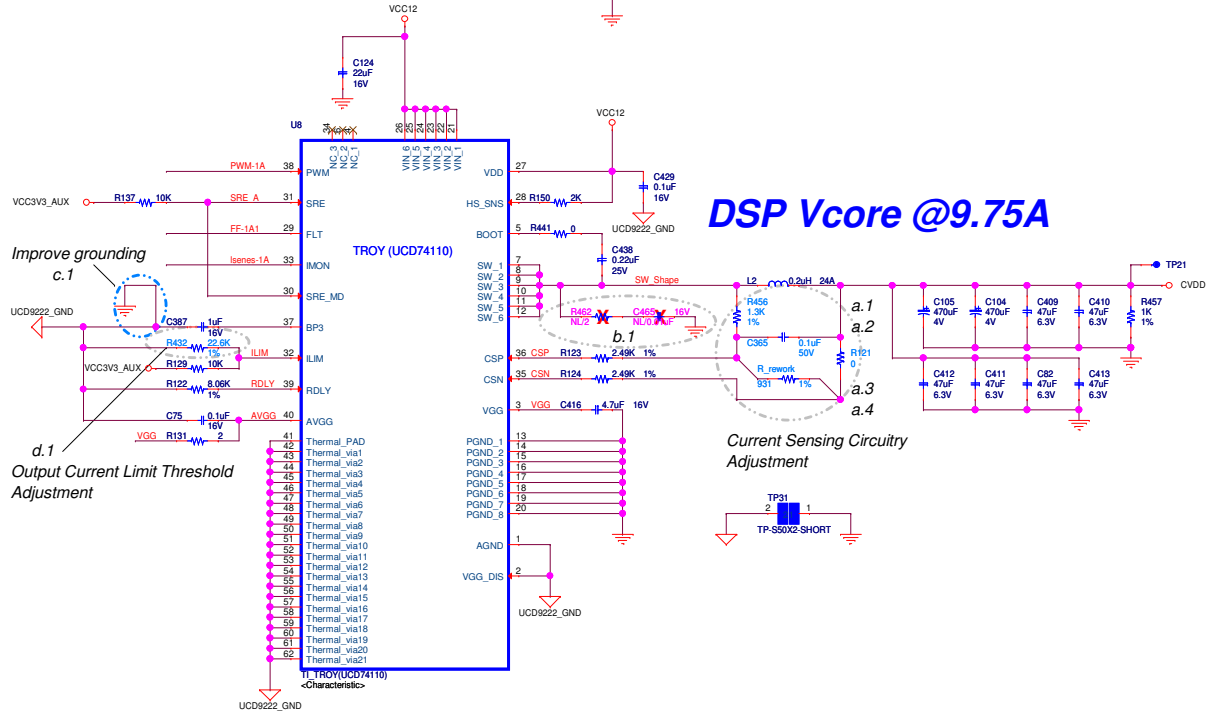


e.1
UC74106 Current Feedback Adjustment

DSP Vcore @9.75A

Improve grounding
c.1

d.1
Output Current Limit Threshold Adjustment



Current Sensing Circuitry Adjustment

TMDXEVM6670L EVM A102-1 Workaround List at page.33 of the schematics:

- a. Modify the DCR sensing circuitry of the L2 inductor
 - a.1 R456 -> 1.3k ohms
 - a.2 C365 -> 0.1uf cap
 - a.3 R121 -> 0 ohm
 - a.4 Additional R -> 931 ohms (across the ends of R123 and R124).
- b. Remove the snubber components.
 - b.1 Remove R462, C465.
- c. Improve the grounding on UCD9222_GND plan.

The UCD9222 was halt in POR state, this was caused by the incorrect connection of the 74110 power pad to analog ground.

- c.1 C387 (BP3, the end of UCD9222_GND) -> GND
- c.2 C54 (ADC_REF, the end of UCD9222_GND)-> GND
- c.3 C28 (BPCAP, the end of UCD9222_GND) -> GND

The changes at the BPCAP pin would not have been needed if the attached capacitor was on the same side and located close to the pin.

- d. Output Current Limit Setting Adjustment on UCD74110.
 - d.1 R432 -> 22.6k ohms
- e. Current feedback setting Adjustment on UCD74106.
 - e.1 R116 -> 10k ohms
- f. Improving UCD9222 V33A power supply.

The additional capacitor on the V33A/V33D pin is needed to improve decoupling of this sensitive circuitry.

Note that this would not have been necessary had the available decoupling been implemented adjacent to the UCD9222 on the same side so that vias were not needed.

f.1 Additional C -> 4.7uf between the pins of V33A and AGND on the UCD9222.